Performance and Tracking Control of Three-Phase Induction-Motor Drive Fed from a DC-Modified Nano-grid

ESSAMUDIN ALI EBRAHIM, ABUELMAATY M. ALI
Power Electronics and Energy Conversion Department,
Electronics Research Institute,
Joseph Tito St., Huckstep, Qism El-Nozha, Cairo Governorate,
Cairo, EGYPT

Abstract: - Nowadays, induction motor (IM) can be used in tracking systems to follow a predefined path like in robotics and servos. The modified nanogrid is a small scalable-renewable energy intermittent source that powers loads via a single-stage inverter (SSI). Higher utilization of the inverter dc-link voltage improves its output voltage and dependently, the performance of the motor tracking speed. So, this paper proposed a switched boost inverter (SBI) to feed IM from PV-source to boost its dc-link output voltage. The usage SBI utilizes minimum passive components, more active elements, introduces shot-through mode as z-source inverter and produces both ac and dc voltages simultaneously. The performance of the motor-tracking speed depends directly on the motor-stator voltage. So, the proposed method depends mainly on two parameters to verify the pre-defined trajectory speed. The first proposed intention is a simple algorithm for a closed-loop dc-link boosting control based-on the reference model to compute the optimum duty ratio (D). The second one depends mainly on the modulation index (M) to produce the reference signals needed to adjust the speed of the motor through a (voltage/frequency) control of a predefined value. The modelling and validation of the proposed system was implicit with the help of Matlab/ Simulink package. The robustness of the system has been tested by selecting several speed trajectories and was able to track them. Furthermore, SBI-based system was compared with other VSI-inverter techniques such as the space-vector PWM single- and two-stage VSI inverters. Test results explored that the SBI-based system is a strong competitor to other inverter techniques especially at low-voltage intermittent supply.

Key-Words: - Induction motor Drive (IM), Photo-voltaic (PV), Switched Boost Inverter (SBI), Modified Nanogrid, Tracking Control


1 Introduction

Induction motors (IMs) are robust, inexpensive, require approximately no maintenance, and have a high power/ weight ratio [1-4]. Voltage and current source inverters (VSI and CSI) are used in adjustable speed drives of IMs [5-7]. Unfortunately, both of them have some limitations. When VSI upper switches or lower switches are turned on simultaneously, a zero state is produced. This means that a short circuit occurs through the VSI-output terminals. This short circuit is called shot-through short circuit. This shot-through is forbidden for each limb of 3-phases for VSI and can destroy all inverter modules. To prevent this overlap which causes this shot-through it should be using a dead-time delay circuit through the triggering process of electronic switches. Classical nanogrid with small rate uses both converter and inverter as a two-stage power converter [8]. Also, the two stage conversion system has drawbacks such that: the conventional h-bridge inverter must provide the output voltage less than the input voltage, and its output cannot be more than the DC-link voltage [9-11]. So, a modified DC nanogrid is introduced to enhance the system operation. The structure of the modified nanogrid proposes a single stage boost inverter (SSBI) with reduced number of conversion systems between the photovoltaic (PV) and nanogrid with residential applications for both AC and DC loads [12,13]. Recent researches proposed Z-source inverter as a single input multi output (SIMO) inverter [14,15]. But, it needs to many reactive components and small number of active switches compared with the other single stage switched boost inverter which leads to increase the system cost, losses and decreases the system efficiency. Some efforts were exerted to improve the performance of Z-source inverter and overcome its defects such as in [16]. A novel power converter called switched boost inverter (SBI) is an
improved copy of ZSI that demonstrates the same features of the z-inverter with using a few number of passive elements. The main features of a SBI that it allows the shot-through and no need to dead-time circuits. Also, it produces both ac and dc simultaneously. So, it can increase its an ac-output voltage to a sufficient value [17-22]. Although the features of SBI, but a few number of researches suggested using SBI with 3-phase IM drives such as in [23,24]. These researches discussed open-loop speed control of IM when feeding it via a SBI. There are rarely researchers that discussed the performance and behaviour of the IM - as a high performance drive- when feed from PV via a SBI. Also, some relevant studies can be found in [25] and [26].

So, this manuscript proposes a simple controller for a 3-phase IM-drive fed from a modified nanogrid with PV array - as a renewable resource - via SBI. The drive is an adjustable-speed high-performance tracking system that can follow a prescribed trajectory for the motor speed. The proposed controller is a simple algorithm to control the drive- when feed from PV via a SBI. The robustness of the controller depends on the combination of both features of SBI, but a few number of researches contributed towards making IM as a high-performance drive. The value of rotor reference and actual frequency and gives a wide speed range for the motor. This helps in increasing the motor speed to reach the rated speed and contributes towards making IM as a high-performance drive. The robustness of the controller is tested by proposing several speed trajectories (such as trapezoidal, random, sinusoidal), and the motor succeeded to follow all of them. In addition, a comparison among space vector PWM 2-stage and 1-stage VSIs was conducted to prove the validity and suitability of the proposed drive in speed tracking.

The organization of the paper is as follows: section 1 is an introduction, the proposed rig is introduced in section 2. Section 3 is a modified nano-grid with PV-array and SBI topology is explained in section 4. The closed-loop control for DC-link voltage with MPPT is introduced in section 5. Section 6 is a validation for the proposed system through test results. Finally, the concluding remarks of the manuscript are given in section 7.

2 The Proposed Rig

Figure 1 depicts the proposed system that implies the improved copy of ZSI that demonstrates the same features of the z-inverter with using a few number of passive elements. The main features of a SBI that it allows the shot-through and no need to dead-time circuits. Also, it produces both ac and dc simultaneously. So, it can increase its an ac-output voltage to a sufficient value [17-22]. Although the features of SBI, but a few number of researches suggested using SBI with 3-phase IM drives such as in [23,24]. These researches discussed open-loop speed control of IM when feeding it via a SBI. There are rarely researchers that discussed the performance and behaviour of the IM - as a high performance drive- when feed from PV via a SBI. Also, some relevant studies can be found in [25] and [26].

Figure 2 represents the block diagram for variable-voltage variable-frequency PI-speed controller. The error signal is the difference between both actual and reference rotor speed in the form of frequency according to the relation:

\[ f = \frac{PN}{60} \]

\[ e(t) = (f_r^* - f_r) \]

Where P is the number of pair-poles, \( f_r^* \), \( f_r \) is the rotor reference and actual frequency and e is the error signal. The output of the PI-controller is the slip-frequency \( f_{slip} \):

\[ f_{slip} = K_p e(t) + K_i \int e(t) dt \]

\[ = K_p (f_r^* - f_r) + K_i \int (f_r^* - f_r) dt \]

Then, the reference synchronous frequency \( f_s^* \) of the motor can be computed as:

\[ f_s^* = f_r + f_{slip} \]

To maintain the internal torque constant, the stator voltage should be changed with the same rate of frequency changing, so the reference voltage peak-
Figure 1. The proposed system

Figure 2 Block diagram for the speed controller (scalar controller $V/f$)

Figure 3. PV-based modified nanogrid

Figure 4 Schematic diagram of switched boost inverter fed 3-phase IM
Figure 5 Equivalent circuit of SBI through interval
(a) D.Ts  (b) (1 − D.Ts)

value \( V_m^* \) can be scaled with frequency according to the relation: \( V_m^* = K_{v f} \cdot f \) 
(7)
Where, \( K_{v f} \) is a constant depends on flux and selected according to the rated frequency and voltage of the machine. For this work, this value is equal 3.2 for the test machine.

3. PV-Based Modified Autonomous Nano-grid

The modified nanogrid consists of renewable energy resource such as PV-array, single-stage inverter such as SBI, storage management system such as batteries, and buck-boost converter as shown in figure 3. This study will focus only on the PV-array model and 3-phase SBI topology. Only, 3-phase IM is considered as an ac load.

3.1. Mathematical Model of PV-Array

Several parameters and aspects are affected on the output power generated by the PV modules. Some of these significant factors are: PV-material type, intensity of solar insolation received by the module, module temperature, parasitic resistances, shading and cloud effect, dust, module location, weather, module orientation, cable resistances, and the efficiency of the inverter. The output is directly proportional to the intensity of solar insolation and inversely proportional to the temperature [27].

Moreover, several mathematical models of the PV module are proposed for simulation. The accurate model is used in this work. It takes into consideration the series and shunt resistances. The cell current is given as follows:

\[ I = I_{irr} - I_o \left( e^{\frac{q(V+iR_s)}{nKT}} - 1 \right) - \frac{V+iR_s}{R_p} \] 
(8)

Then, the current of the module is given as:

\[ I_m = I_{irr} - I_o \left( e^{\frac{q(V+iN_pR_s)}{nKnKT}} - 1 \right) - \frac{V+iN_pR_s}{N_pR_s} \] 
(9)

The array model includes several series and parallel modules [28-29]. In an array, PV modules are connected in series and in parallel. The array current \( I_g \), terminal voltage \( V_g \) and has \( N_S \) cells in series and \( N_P \) strings in parallel can compute all by using the following equation [30]:

The PV- array current is:

\[ I_g = N_P I_{irr} - I_o \left( e^{\frac{q(V_g+iA_{irr}N_P)}{N_PKnKT}} - 1 \right) - \frac{V_g+iA_{irr}N_P}{N_PR_P} \] 
(10)

\[ V_g = \frac{N_P}{N_S} \ln \left[ \frac{G_N I_{PV} - I_{irr}}{I_P N_S} + 1 \right] - \frac{N_S}{N_P} R_s I_g \] 
(11)

And \( \Lambda = \frac{q}{eKT} \)

Also, the total output of the PV array can be determined as:

\[ P_g = V_g \times I_g \] 
(12)

Where, \( P_g \) is the mean power of the array in (W), \( I, I_g \) module and array currents in (A), \( I_{irr} \) is the current of irradiance (A), \( I_o \) is the saturation current of diode (A), \( V_m, V_{PV} \) are the module and array voltages (V), \( q = 1.6 \times 10^{-19} \) C is the electron charge in (C), \( K = \text{Boltzmann constant and equal } 1.3806503 \times 10^{-23} \text{J/K} \), \( R_s, R_p \) are series and parallel resistances (\( \Omega \)), \( N_s \) and \( N_p \) are number of series and parallel cells, \( T=298.15 \text{is the temperature of the cell (ºK)} \), \( \epsilon = 1.1 \) is the completion factor, \( \Lambda \) is the solar cell constant, \( G \) is the solar insulation in per unit.

4. The Proposed Switched Boost Inverter (SBI) Topology

SBI – as shown in figure (4) - it consists of two diodes, one inductor, one capacitor, 7-IGBTs, and an L-C passive filter for harmonic mitigation. As the ZSI, it utilizes the shoot-through state of the inverter to boost its output voltage \( V_c \). The shot-through zero-state is assumed to be acted through the interval \((D.T_s)\) and the mathematical equation of SBI through this period can be obtained - with the help of its equivalent circuit shown in figure (5) (a) and (b)- as follows [31]:

\[ v_L(t) = v_C(t), i_C(t) = -i_L(t), v_i(t) = 0 \] 
(13)

But, through the interval \((1 − D).T_s\), the equation of the model is given as:

\[ v_L(t) = V_g - v_C(t), i_C(t) = i_L(t) - i_i(t), v_i(t) = v_C(t) \] 
(14)

During the interval \( T_s \), the average voltage across L equal to zero, then:

\[ \frac{v_C}{V_g} = \frac{1-D}{1-2D} \] 
(15)
4.1 The Proposed Intension Modulation-Index Speed Control

As seen from equation 15 and similar to ZSI, the duty ratio \((D)\) of SBI is limited from 0 to 0.5. Also, the relation between modulation index \(M\) and \(D\) is determined according to the following criteria:

\[
M + D \leq 1
\]  
(16)

The maximum peak value for ac-output voltage for the line can be determined as:

\[
V_m = MV_C = \frac{(1-D)MV_g}{(1-2D)}
\]  
(17)

Then, the three-phases output voltages that supply the motor are:

\[
V_{as} = V_m \sin(\omega t), \quad V_{bs} = V_m \sin(\omega t - 2\pi/3), \quad V_{cs} = V_m \sin(\omega t + 2\pi/3)
\]  
(18)

By substituting in eqn. 18 from 17, it can be obtain the stator input voltage as a function of \(M\) as follows:

\[
V_{as} = \frac{(1-D)MV_g}{(1-2D)} \sin(\omega t)
\]  
(19)

Figure 6 explores the block diagram to generate the required control signals of SBI. Six signals are required for six-pulse switches and another one for shot-through. The reference signal is a sinusoidal that can be obtained from the two reference control signals. They can be computed as a sinusoidal that can be obtained from the two carrier wave.

\[
V_B = V_m \sin(\omega t)
\]  
(20)

Figure 7 (a) shows the peak value for the triangular carrier wave

\[
V_{tri} = \begin{cases} 
-\frac{V_p}{T_s} (t - \frac{T_s}{4}) & \text{if } 0 < t < \frac{T_s}{4} \\
\frac{V_p}{T_s} (t - \frac{3T_s}{4}) & \text{if } \frac{T_s}{2} < t < T_s 
\end{cases}
\]

Where, \(T_s\) is the switching time for carrier wave and \(f_s\) is the switching frequency of SBI.

And the shot-through modulated signals \(+V_{ST}\) and \(-V_{ST}\) - shown in figure 7b - can be computed as:

\[
+V_{ST} = V_p (1 - D), \quad -V_{ST} = -V_p (1 - D)
\]  
(21)

4.1.1. Generation of gating control signals \((G_{s1}, G_{s3}, G_{s5})\) of SBI

The complete cycle for the modulation sinusoidal wave form is divided to four intervals as shown in Table 1 and demonstrated in figure 7 (b). The state (high or low) for each interval depends on the value of the modulation signal to the carrier wave. Table one shows only the low state condition for switches \((G_{s1}, G_{s3}, G_{s5})\). The high state will be achieved when all conditions in this table are not satisfied.

4.1.2. Generation of gating control signals \((G_{s2}, G_{s4}, G_{s6})\) of SBI

In traditional VSI, the control gating pulses for lower switches are the complementary to upper switches. But, this is not true for SBI. Table 2 also describes the four intervals and conditions to generate (high/low) states for the lower switches.

4.1.3. Generation of gating control signal \((G_S)\) of SBI

Switch S is turned on in shot-through period. And it will be turned off when it is in non-shot through state. The non-shot-through state can be achieved when all control signals for each leg of the inverter are complementary to others. However, \(G_S\) is generated when \(-V_{ST} < V_{tri} < +V_{ST}\). For more details, it can be referred to ref. [31].

The modulation index can be computed from the following relation:

\[
M = \frac{V_{mp}}{V_p}
\]  
(22)

By substituting from eqn. (3) and (7) in (22),

\[
M = \frac{V_{mp}}{V_p} = K_v f_s \frac{f_s^2}{V_p} = K_v f_s \frac{P_{SN}}{50.6 V_p}
\]  
(23)

From (23), \(M\) is directly proportional to speed, where, \(V_p, P_{SN}\), and \(P\) are constant. Then, it can substituting in eqn. (19) as:

\[
V_{mp} \sin(\omega t) \rightarrow V_{s1}^*, \quad V_{mp} \sin(\omega t + 2\pi/3) \rightarrow V_{s3}^*, \quad V_{mp} \sin(\omega t + 4\pi/3) \rightarrow V_{s5}^*, \quad \text{LOGIC}
\]

\[
\text{GATING SIGNALS GENERATION BLOCK}
\]

\[
V_{tri} \rightarrow V_{tri}^*
\]

\[
\text{GS GS1 GS2 GS3 GS4 GS5 GS6}
\]

Figure 6 SBI-reference voltage waveform generation with pulse-width modulation block
Table 1. Gate control signal generation for $S_1$, $S_3$, $S_5$

<table>
<thead>
<tr>
<th>Interval No.</th>
<th>Value of o/q</th>
<th>Condition for gate control signal to be low</th>
<th>$G_{S1}$</th>
<th>$G_{S3}$</th>
<th>$G_{S5}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0°-90°</td>
<td>$V_a^* &gt; V_{tri}$</td>
<td>$V_b^* &lt; V_{tri} &lt; V_{ST}$</td>
<td>$V_c^* &lt; V_{tri}$</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>90°-210°</td>
<td>$V_a^* &lt; V_{tri}$</td>
<td>$V_b^* &lt; V_{tri}$ &lt; $V_{ST}$</td>
<td>$V_c^* &lt; V_{tri}$</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>210°-330°</td>
<td>$V_a^* &lt; V_{tri} &lt; V_{ST}$</td>
<td>$V_b^* &lt; V_{tri}$ &lt; $V_{ST}$</td>
<td>$V_c^* &lt; V_{tri}$</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>330°-360°</td>
<td>$V_a^* &lt; V_{tri}$</td>
<td>$V_b^* &lt; V_{tri} &lt; V_{ST}$</td>
<td>$V_c^* &lt; V_{tri}$</td>
<td></td>
</tr>
</tbody>
</table>

Table 2. Gate control signal generation for $S_2$, $S_4$, $S_6$

<table>
<thead>
<tr>
<th>Interval No.</th>
<th>Value of o/q</th>
<th>Condition for gate control signal to be low</th>
<th>$G_{S1}$</th>
<th>$G_{S3}$</th>
<th>$G_{S5}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0°-30°</td>
<td>$V_a^* &gt; V_{tri}$</td>
<td>$V_b^* &gt; V_{tri}$ &lt; $V_{ST}$</td>
<td>$V_c^* &gt; V_{tri} &gt; -V_{ST}$</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>30°-150°</td>
<td>$V_a^* &gt; V_{tri} &gt; -V_{ST}$</td>
<td>$V_b^* &gt; V_{tri} &gt; -V_{ST}$</td>
<td>$V_c^* &gt; V_{tri} &gt; -V_{ST}$</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>150°-270°</td>
<td>$V_a^* &gt; V_{tri}$</td>
<td>$V_b^* &gt; V_{tri} &gt; -V_{ST}$</td>
<td>$V_c^* &lt; V_{tri}$</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>270°-360°</td>
<td>$V_a^* &gt; V_{tri}$</td>
<td>$V_b^* &gt; V_{tri}$ &lt; $V_{ST}$</td>
<td>$V_c^* &gt; V_{tri} &gt; -V_{ST}$</td>
<td></td>
</tr>
</tbody>
</table>

5. Proposed Closed-Loop DC-Link Voltage Control

5.1. Maximum Power Point Tracking (MPPT) Algorithm

There are several proposed techniques for maximum power point (MPP) operation as in [32]. However, this study uses incremental conductance method – as a simple algorithm - to determine MPPT [33]:

$$\frac{dP_g}{dV_g} = \frac{d(V_g d_{pg})}{dV_g} = I_g + V_g \frac{dI_g}{dV_g} = 0$$  \hspace{1cm} (24)

By approximately linearizing the relationship of grid current by substituting from (10) and (11) in (24):

$$I_g = I_{MPP} \approx 0.94 I_{sc}$$  \hspace{1cm} (25)

Where, $I_{MPP}$ is the array output current at a MPP and $I_{sc}$ is a short circuit current for the PV array.

By integrating the eqn. (24) to minimize the error, this gives the duty ratio that achieves MPPT ($D_{mpp}$) as:

$$D_{mpp} = \int K_m \left( \frac{I_g}{V_g} + \frac{dI_g}{dV_g} \right) dt + D_0$$  \hspace{1cm} (26)

5.2. The Proposed Model as a Reference for DC-Link Voltage Control

Figure 8 depicts the block diagram for both voltage and current control, this block produces the voltage level control signal ($V_{ST}$) that is needed for shot-through signals controlling the switch ($S$).

The control algorithm depends on introducing a mathematical model for the actual value of $V_{ST}$ that can be computed as follows [34]:

From eqn. 21, $D = 1 - \frac{V_{ST}}{V_p}$  \hspace{1cm} (27)

If $V_p = 2$, by substituting in eqn. 27,

$$D = 1 - \frac{V_{ST}}{2}$$  \hspace{1cm} (28)

By substituting in eqn. 15 from 28,
6. TEST RESULTS AND DISCUSSION

To test the robustness of the proposed system with the IM as a high performance drive, a software package is introduced with the help of Matlab/Simulink program. The main data of 1-hp 3-phase IM as a test machine is included in Tab. 3. This data was obtained from references [1] and [2]. Also, the main parameters of the SBI are also modelled and implied in Tab. 4. The data of this table was collected from references [23],[24],[31] and [34]. In this study, several speed trajectories are proposed for a wide speed range to test the robust of the proposed controller with SBI. Also, these proposed trajectories include an acceleration, deceleration, 2-quadrant operation of the motor. In addition, results include the PV-variable profiles with time such as irradiation, the output voltage \( V_g \), power \( P_g \), and some control signals such as modulation 3-phase reference signals \( V^* \), and triangular signal \( V_{tri} \). Four cases are studied. All results for each case include dependently other parameters variation such as motor and load torque, stator current and dc-link voltage of the inverter.

All cases including the same proposed irradiance profile shown in figure (9-a) to test the robustness of the controller against PV-parameter variations. The gain values for speed-controller are selected as \( K_p = 5 \, \text{and} \, K_i = 12 \) for all trajectories. Also, these values for dc-link voltage proposed controller are \( K_p = .00001 \, \text{and} \, K_i = 0.00005 \) for both voltage and current loops with limiting block value \( \mp 2 \).

Table 3. The name plate data of the test machine

<table>
<thead>
<tr>
<th>parameter</th>
<th>value</th>
<th>unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rated voltage</td>
<td>220</td>
<td>V</td>
</tr>
<tr>
<td>Rated power</td>
<td>1</td>
<td>hp</td>
</tr>
<tr>
<td>Rated frequency</td>
<td>50</td>
<td>Hz</td>
</tr>
<tr>
<td>Rated line current</td>
<td>2.3</td>
<td>A</td>
</tr>
<tr>
<td>Rated speed</td>
<td>960</td>
<td>Rpm</td>
</tr>
<tr>
<td>No. of pair poles (P)</td>
<td>3</td>
<td>poles</td>
</tr>
<tr>
<td>Stator resistance (R_s)</td>
<td>10.5</td>
<td>Ω</td>
</tr>
<tr>
<td>Rotor resistance referred to stator (R'_s)</td>
<td>11.08</td>
<td>Ω</td>
</tr>
<tr>
<td>Stator leakage reactance (X_s)</td>
<td>11.03</td>
<td>Ω</td>
</tr>
<tr>
<td>Rotor leakage reactance referred to stator (X_r)</td>
<td>11.09</td>
<td>Ω</td>
</tr>
<tr>
<td>Magnetizing Reactance (X_m)</td>
<td>126.3</td>
<td>Ω</td>
</tr>
<tr>
<td>Rotor moment of inertia</td>
<td>.042</td>
<td>Kgm²</td>
</tr>
</tbody>
</table>

Table 4. The values of SBI parameters

<table>
<thead>
<tr>
<th>parameter</th>
<th>value</th>
<th>unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Grid-input voltage (V_g)</td>
<td>255</td>
<td>V</td>
</tr>
<tr>
<td>Limitation of DC-link voltage (V_c)</td>
<td>622</td>
<td>V</td>
</tr>
<tr>
<td>Fundamental frequency (f)</td>
<td>50</td>
<td>Hz</td>
</tr>
<tr>
<td>Switching Frequency (f_{sw})</td>
<td>10</td>
<td>kHz</td>
</tr>
<tr>
<td>Inductance of main inductor (L)</td>
<td>5</td>
<td>mH</td>
</tr>
<tr>
<td>Capacitance of main capacitor (C)</td>
<td>1500</td>
<td>μF</td>
</tr>
<tr>
<td>Inductance of filter inductor (L_f)</td>
<td>1</td>
<td>mH</td>
</tr>
<tr>
<td>Capacitance of filter capacitor (C_f)</td>
<td>150</td>
<td>μF</td>
</tr>
</tbody>
</table>
The selected module for the solar panel and its technical specification is given in appendix A. The module specifications were extracted from NREL System Advisor Model (website [35]). Its type model is Sunpower SPR-305-WHT-U (305W) Solar Panel. The PV array consists of 5 modules all connected in series to give approximately 1.5 kW at 270 V or connected in parallel to give 54 V. SBI has many advantages but the main drawback is its duty ratio is limited to 0.5. So, the proposed control is used to overcome this defect, especially, though the instability region (0.45 to 0.5). Also, the main purpose of the controller is keeping the DC-link voltage of SBI constant with simultaneously MPP achievement. Keeping DC-link constant insures a perfect control for predetermined speed trajectories. Also, the modulation index contributes towards tracking the trajectory without deviation. So, both \( D \) and \( M \) profiles with time are plotted to show their variation effects through the speed trajectories. In addition, the test results imply a comparison between the proposed SBI and VSI with and without converter using space-vector PWM [36].

All parameters are the same for each system, i.e., the same speed controller gains, the same \((V/f)\) control technique with the same parameters. Through the results, the VSI- without converter is considered as a single-stage and denoted by (SVPWM-1) and with converter is a two-stage and denoted by (SVPWM-2). Also, all cases are including variable load torque with time. Figures (9-b and c) demonstrate the PV-output voltage and generated power according to the proposed insolation profile shown in fig. 8a. As shown, both of them affected by the irradiance variation. Also, figure (9-d) represents the three-phase AC-output voltage for SBI - as a sample - for input to the stator. In addition, figures (9-e and f) are

Fig. 9 (a) PV-irradiance (b) PV-terminal voltage (c) PV-mean power (d) 3-phase SBI voltage (e) 3-phase reference signals (f) carrier wave
the 3-phase modulation and carrier waves used in pulse generation for IGBTs of SBI.

6.1. Case (1): Trapezoidal Speed Trajectory

In this case, the speed trajectory is a trapezoidal to test the acceleration and deceleration in forward rotation. The number of series panels are 5 to give approximately 1.5 kW solar power at around 270V input to the SBI with the same insolation profile described in figure 8-a. The steady state speed for the trajectory is the motor-rated speed (i.e., 960 rpm). Figure 10-a explores actual and reference trapezoidal speed trajectories. Through acceleration region, there is a slight difference between the reference and actual, then there is a matching between both with some oscillations at the nominal motor speed region and then some deviations through the deceleration. Figure 10-b describes the motor current through this track. Also, the motor and load torques are shown in figure 10-c. There is some oscillations and deviation between them through acceleration and deceleration. The dc-link voltage profile that contributes in tracking achievement is shown in figure 10-d. Although the insolation of the PV was reduced from 1000 to approximately 200 W/m² as shown in fig. 9-a, but the proposed controller boosted the dc-link voltage to suitable tracking-speed values. When irradiation is low, the duty ratio decreases with the help of the proposed robust controller to increase the DC-link voltage and vice versa. When the insolation increases, the terminal voltage maintains constant by increasing duty ratio of the inverter. The profile of both D and M with time are illustrated in figures 10 (e) and (f) respectively. As shown, the D is decreased with the insolation reduction and vice versa. Also, M is directly proportional to the speed. M-profile is approximately the same as the speed trajectory with some oscillations through transient.

![Figure 10 Case 1: Trapezoidal speed trajectory](image-url)
6.2. Case (2): Random Forward Speed Trajectory (with a Comparison)

In this case, the speed trajectory was selected randomly with including acceleration and sharp-deceleration in forward motor rotation. The same input voltage – as in case 1 – is used (i.e., $V_g = 270V$). The motor is accelerated in steps from zero to (600 rpm) and then raised to nominal speed. After that, the motor decelerated from rated speed to 400 rpm. The acceleration and deceleration act through very short period. This case includes a comparison among SBI, SVPWM-1 and SVPWM-2. Figures 11-a and b describe the actual and reference speed trajectories for all inverters. SBI and SVPWM-2 are successfully tracked the speed-path with some oscillations. But, SVPWM-1 has some deviation through the path due to un-controlled dc-link input voltage. For enlarged stator currents shown in figure
11-c, through deceleration as an example, there are some distortions for the waveforms of both SVPWM-1 and -2 through speed transition from rated to 400 rpm. But, the torque shown in figure 11-d, there are some deviation between the load and the motor internal torque for all types especially through starting and transition of deceleration. However, SBI in black colour, exerted more stable torque compared with other types, especially, SVPWM-1. This can be noticed from figures 11-e and f that explore the dc-link and stator line voltages for all types. SBI has a high value of the dc-link voltage compared to other types. This improves the performance of the motor through tracking of the predefined path.

6.3. Case (3): Sinusoidal Reverse Rated-Speed Trajectory Low-Input Voltage (with a Comparison)

In this case, the speed trajectory is a sinusoidal wave form with its peak value equal to the motor rated speed. The direction of rotation is reversed. The motor operates in the second quadrant mode to test the robustness of the proposed controller with another different speed trajectory. In addition, to test it against PV-parameter variation, the PV-terminal voltage was reduced to a minimum value. So, only one PV panel is connected in series with five in parallel to give approximately 54V as input to the inverters. As shown in figure (12-a and b), SBI and SVPWM-2 are successfully tracked the path with a slight deviation. But SVPWM-1 couldn’t track successfully the path, especially, through the rated speed. This can be clearly shown through the current profile shown in fig. 12c, the waveform is completely distorted but for others, the currents are sinusoidal. On the other hand, SBI demonstrates a good damping dynamic response through load changes shown in fig. 12d. Although the PV-input voltage was reduced but
6.4. Case (4): Sinusoidal Forward Low-Speed Trajectory (with a Comparison)

In this case, the robustness of the controller is tested at very low speed for the motor. The proposed speed trajectory is a sinusoidal wave form with its peak value equal to 500 rpm. The direction of rotation is forward. Also, only 5 PV panels are connected in series to give approximately 270V as input to the inverters. As shown in figure (13-a and b), SBI and SVPWM-2 are successfully tracked the path with a slight deviation. But SVPWM-1 couldn’t track successfully the path, especially, through the top speed-curve. This can be clearly shown through the current profile, the waveform is completely distorted but for other type, the current is a sinusoidal.
Also, the DC-link voltage of SBI is the greatest one as shown in figure 13e and dependently the homogeneity of the AC-voltage fed to the stator is shown in figure 13f.

To test the SBI-output current waveforms homogeneity, Fourier fast transformation (FFT) is used to study the harmonics and compute the total harmonic distortion (THD). From the analysis (for ramp speed trajectory not included here), the THD for the current waveform of SBI 4.57% as shown in fig. 14. This value is low and within the standard IEEE range.

Furthermore, a combination of duty ratio (D) and modulation index (M) of SBI contributed to overcome the drawbacks of SBI. Two proposed controllers are used to modify both D and M to follow the path. The comparison between SBI, VSI-based SVPWM-1 and -2 are conducted to explore the validity of the proposed system in tracking. SBI has many features such as no need for dead time circuits and single-stage inverter that produces both DC and AC simultaneously. In addition, it needs to a few numbers of passive elements. So, test results insure the validity of the SBI as a good in tracking systems.

Appendix A

STC Power = 305W, PTC power =208.6W, Current at maximum Power=5.58A, Voltage at maximum power=54.7V, Short circuit current=5.96A, Open-circuit voltage=64.2V, Number of cells=96, Maximum system voltage =600.

7. Conclusion

This paper investigated the performance and behaviour of IM as a high performance when supplied from a modified PV-based nanogrid via a SBI. The IM served as a tracking system to follow some proposed prescribed random and homogenous speed trajectories. With the help of the proposed cruise speed control, the motor succeeded in following most tracks through using a scalar (V/f) technique. The motor was operating with acceleration and deceleration modes in forward and reverse direction of rotation with rated and lower speeds. In addition, the proposed control made SBI robust against the transmitted intermittent renewable PV solar power to the motor in a sinusoidal waveform with balancing between achieving MPPT and dc-voltage stability. Although the PV- output voltage changes due to the irradiance variation, the proposed SBI-controller was able to boost the DC-link voltage and dependently the AC-output voltage increased to produce the required tracking speed.

References:


[35] https://sam.nrel.gov/


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E-ISSN: 2224-350X
21
Volume 16, 2021